ABSTRACT OF THE DISCLOSURE

A simulation coverage calculating apparatus is provided for calculating a simulation coverage for a complete verification. The apparatus includes a first input unit, a second input unit, an effective test pattern calculating unit, a coverage ratio calculating unit, and an output unit. The first input unit reads a property that represents effective test patterns to a logic circuit to be verified. The effective test pattern calculating unit calculates the effective test patterns based on the property. The second input unit reads the test patterns entered and executed by a verifier. The coverage ratio calculating unit calculates a coverage ratio from a ratio of the number of the test patterns matched to the effective test patterns to the number of all of the effective test patterns. The output unit outputs the calculated coverage ratio.